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1/2-FAMPAT-@Questel**Family Accession Nbr**

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Patent Number

US6269352 B1 20010731 [US6269352]

**STG:** Granted patent as first publication**AP :** 1999US-0461674 19991214**FD :** Continuation of: US731426 19961015 [1996US-0731426]**FD :** Continuation of: US6032140**Title**

Low-voltage, very-low-power conductance mode neuron

Abstract

(US6269352)

A neural network including a number of synaptic weighting elements, and a neuron stage; each of the synaptic weighting elements having a respective synaptic input connection supplied with a respective input signal; and the neuron stage having inputs connected to the synaptic weighting elements, and being connected to an output of the neural network supplying a digital output signal. The accumulated weighted inputs are represented as conductances, and a conductance-mode neuron is used to apply nonlinearity and produce an output. The synaptic weighting elements are formed by memory cells programmable to different threshold voltage levels, so that each presents a respective programmable conductance; and the neuron stage provides for measuring conductance on the basis of the current through the memory cells, and for generating a binary output signal on the basis of the total conductance of the synaptic elements.

Filing Details

(US6269352)

Continuation of: US731426 19961015 [1996US-0731426]

Continuation of: US6032140

Priority Details

1995EP-0830433 19951013

1996US-0731426 19961015

1999US-0461674 19991214

Inventor(s)

FABBRIZIO VITO; COLLI GIANLUCA; KRAMER ALAN

Patent Assignee

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Orig. Patent Assignee

STMicroelectronics S.r.l., Agrate Brianza [IT]

1/1-LGST-@EPO**Patent Number**

US6269352 B1 20010731 [US6269352]

**Application Number**

US46167499 19991214 [1999US-0461674]

Publication actions

19991214 US-AP [POS; EXM]

FILING DETAILS

US46167499 19991214 [1999US-0461674]

20010731 US-B1 [POS; PIF]

Granted patent as first publication

US6269352 B1 20010731 [US6269352]

Action Taken (Eng)

20060214 US/RF-A [OPP]

REISSUE APPLICATION FILED

EFFECTIVE DATE: 20030731

Lastest Event Group

1 of 1 DOCUMENT

UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT

6269352

[Link to Claims Section](#)

July 31, 2001

Low-voltage, very-low-power conductance mode neuron

REISSUE: July 31, 2003 - Reissue Application filed Ex. Gp.: 2122; Re. S.N. 10/631,323 (O.G. February 14, 2006)

APPL-NO: 461674 (09)

FILED-DATE: December 14, 1999

GRANTED-DATE: July 31, 2001

ASSIGNEE-AT-ISSUE: STMicroelectronics S.r.l., Agrate Brianza, Italy (IT), Foreign company or corporation (03)

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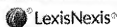
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